

circuit at a first location to produce processed communication signals;

enciphering the processed communication signals in the first signal processing circuit at said first location to produce enciphered and processed communication signals;

transmitting the enciphered and processed communication signals between a first location and a second location using the first communications controller circuit at said first location;

receiving the enciphered and processed communication signals at the second location using a second communications controller circuit;

deciphering the enciphered and processed communication signals in a second signal processing circuit within the second communications controller circuit at said second location; and

processing the deciphered and processed communication signals in the second signal processing circuit to produce communications signals at the second location.

21. (Amended) A system for privately communicating communications signals over a wireless communications network, comprising:

a first communications controller at a first location;
a first signal processing circuit within a first communications controller circuit at a first location for processing communications signals to form processed communication signals and further enciphering said processed communication signals;

a first transceiver associated at said first location with said first communications controller for transmitting

said enciphered and processed communication signals between
said first location and a second location;

a second communications controller circuit at the second
location for controlling communications at said second
location;

a second transceiver associated at the second location
with said second communications circuit for receiving said
enciphered and processed communication signals from said first
transceiver;

a second signal processing circuit within said second
communications controller circuit at the second location for
deciphering said received enciphered and processed
communication signals, said second signal processing circuit
further for processing

Claim 26 ~~at line 10, change "19" to--21--.~~

Claim 35 ~~at line 29, change "19" to--21--.~~

Claim 41 ~~at line 1, change "19" to--21--.~~

Claim 42 ~~at line 21, change "19" to--21--.~~

Claim 43 ~~at line 10, after "controller", insert--
circuit--.~~

Claim 44 ~~at line 14, change "39" to--43--.~~

Claim 45 ~~at line 18, change "39" to--43--.~~

Claim 46 ~~at line 23, change "41" to--45--.~~

Claim 47 ~~at line 29, change "41" to--45--.~~

Claim 52 ~~at line 22, change "41" to--45--.~~

Claim 60 ~~at line 22, change "41" to--45--.~~